

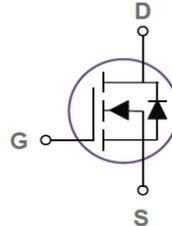
## Silicon N-Channel Power MOSFET

### Features

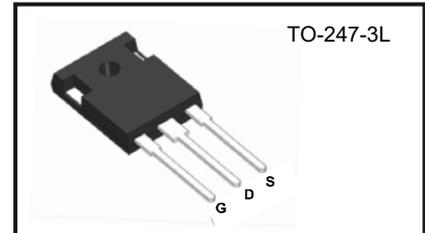
- Fast Switching
- Low On-Resistance
- Low Gate Charge Minimize Switching Loss
- Fast Recovery Body Diode
- 100% Single Pulse Avalanche Energy Test

### Applications

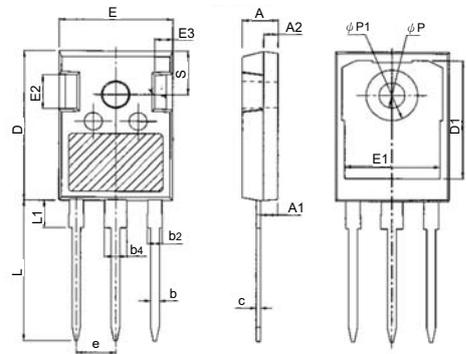
- Adaptor
- Charger
- SMPS Standby Power



$V_{DSS}$	1500V
$I_D(@25^{\circ}C)$	6A
$R_{DS(ON)}$ typ.	2.5 $\Omega$



Package Dimensions



### Absolute Maximum Ratings

( $T_c = 25^{\circ}C$  unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Drain Source Voltage	$V_{DS}$	1500	V
Gate Source Voltage	$V_{GS}$	$\pm 30$	V
Drain Current Continuous @ $T_c = 25^{\circ}C$ @ $T_c = 100^{\circ}C$	$I_D$	6 4.4	A
Drain Current Pulsed	$I_{DM}$	24	A
Single Pulse Avalanche Energy	$E_{AS}$	200	mJ
Power Dissipation @ $T_c = 25^{\circ}C$	$P_D$	300	W
Storage Temperature Range	$T_{STG}$	-55 to +150	$^{\circ}C$
Operating Junction Temperature Range	$T_J$	-55 to +150	$^{\circ}C$
Thermal Resistance Junction to Case	$R_{\theta Jc}$	0.42	$^{\circ}C/W$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	40	$^{\circ}C/W$

UNIT:mm			
Symbol	Min.	Nom	Max.
A	4.80	5.00	5.20
A1	2.21	2.41	2.61
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.61	0.75
D	20.70	21.00	21.30
D1	16.25	16.55	16.85
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44BSC		
L	19.62	19.92	20.22
L1	-	-	4.30
$\phi P$	3.40	3.60	3.80
$\phi P1$	-	-	7.30
S	6.15BSC		

## Electrical Characteristics @ T<sub>c</sub> =25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>OFF Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V , I <sub>DS</sub> =0.25mA	1500	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> =0V , V <sub>DS</sub> =1500V	-	-	10	μA
Gate To Source Forward Leakage	I <sub>GSS(F)</sub>	V <sub>GS</sub> =±30V , V <sub>DS</sub> =0V	-	-	±100	nA
<b>ON Characteristics</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =0.25mA	2.5	-	4.5	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V , I <sub>DS</sub> =3A	-	2.5	3.5	Ω
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =15V , I <sub>D</sub> =3A	-	2.0	-	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V	-	3800	-	pF
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> =0V	-	200	-	
Reverse Transfer Capacitance	C <sub>rss</sub>	Freq.=1MHz	-	26	-	
<b>Switching Characteristics</b>						
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =750V	-	45	-	ns
Rise Time	t <sub>r</sub>	V <sub>GS</sub> =10V	-	20	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	I <sub>D</sub> =3A	-	70	-	
Fall Time	t <sub>f</sub>	R <sub>G</sub> =10Ω	-	35	-	
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =750V	-	19	-	nC
Gate to Source Charge	Q <sub>gs</sub>	V <sub>GS</sub> =10V	-	30	-	
Gate to Drain Charge	Q <sub>gd</sub>	I <sub>DS</sub> =3A	-	11	-	
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V , I <sub>S</sub> =6A	-	-	5	V
Continuous Source Current (Body Diode)	I <sub>SD</sub>		-	-	6	A
Max. Pulsed Current (Body Diode)	I <sub>SM</sub>		-	-	24	A
Reverse Recovery Time	T <sub>rr</sub>	V <sub>GS</sub> =0V I <sub>S</sub> =6A , T <sub>J</sub> =25°C	-	300	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>	di <sub>r</sub> /dt=100A/μs	-	1900	-	nC

\*Pulse Width < 380 μs, Duty Cycle < 2%.

Typical Performance Characteristics

Fig. 1. Output Characteristics @  $T_J = 25^\circ\text{C}$

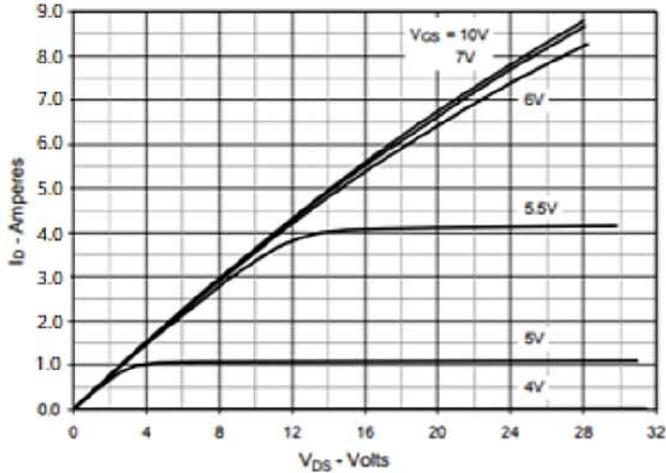


Fig. 2. Output Characteristics @  $T_J = 125^\circ\text{C}$

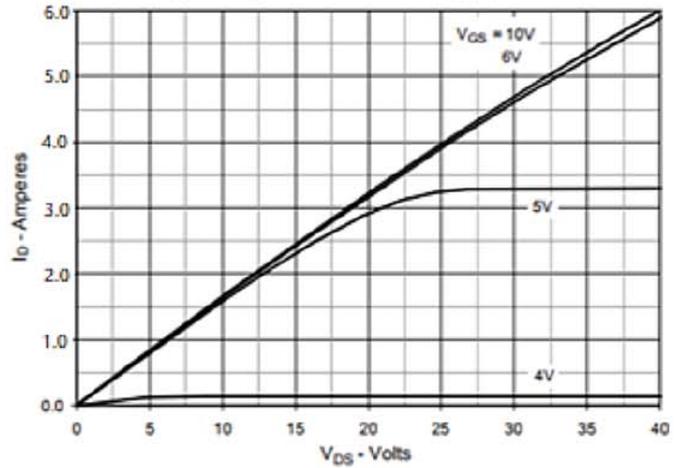


Fig. 3.  $R_{DS(on)}$  Normalized to  $I_D = 3.0\text{A}$  Value vs. Junction Temperature

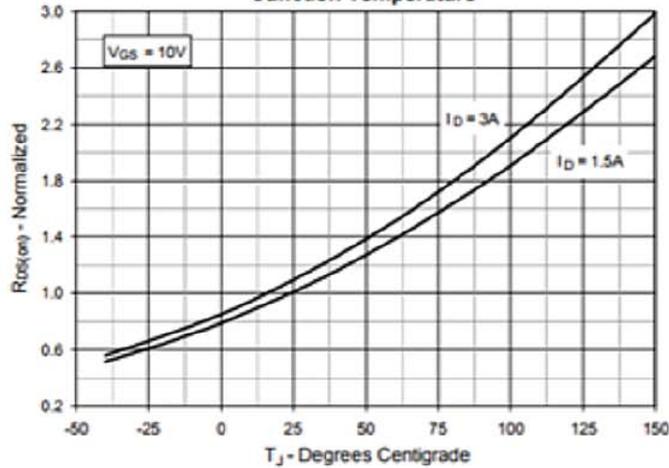


Fig. 4.  $R_{DS(on)}$  Normalized to  $I_D = 3.0\text{A}$  Value vs. Drain Current

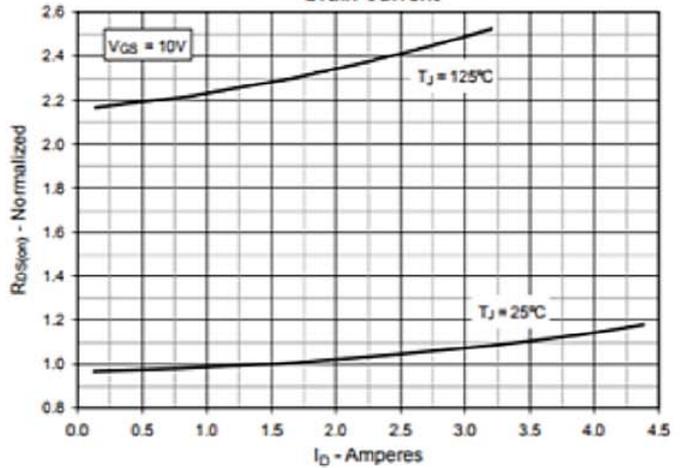


Fig. 5. Maximum Drain Current vs. Case Temperature

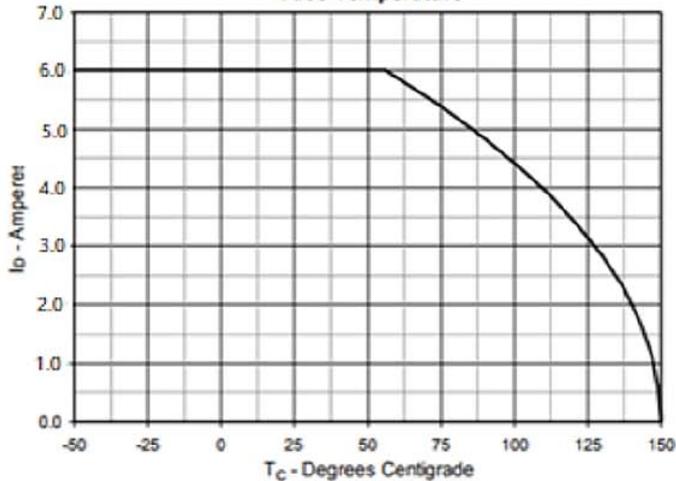
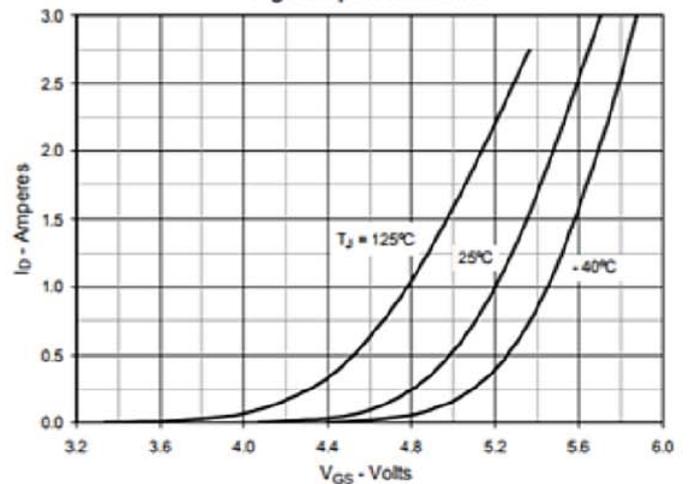


Fig. 6. Input Admittance



Typical Performance Characteristics

Fig. 7. Transconductance

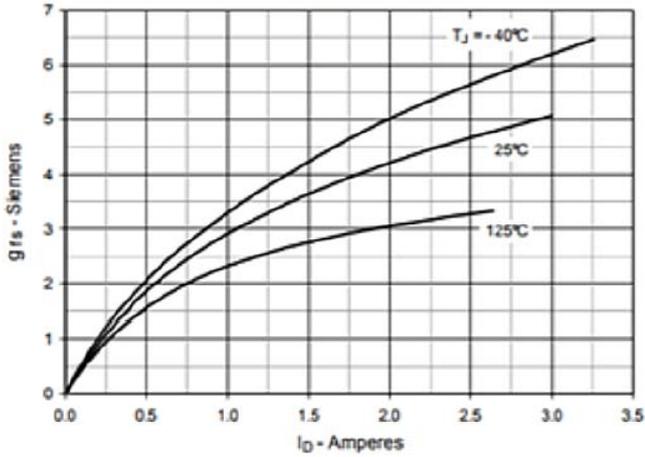


Fig. 8. Forward Voltage Drop of Intrinsic Diode

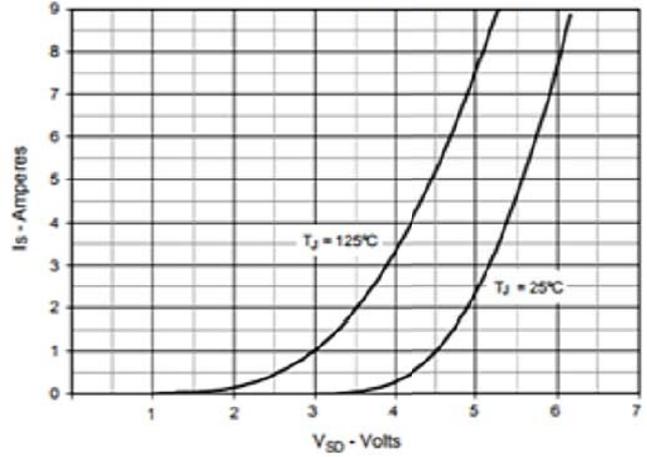


Fig. 9. Gate Charge

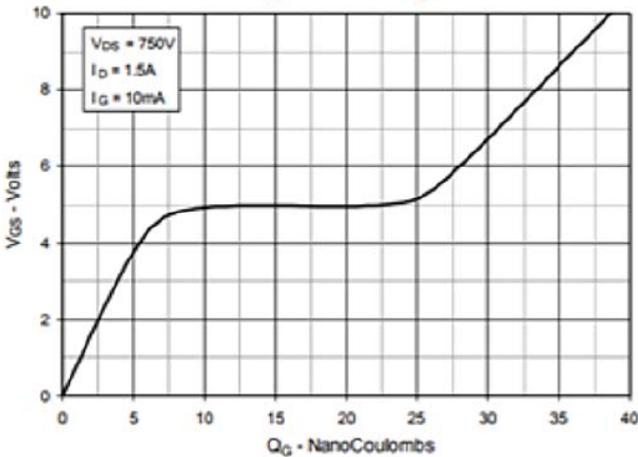


Fig. 10. Capacitance

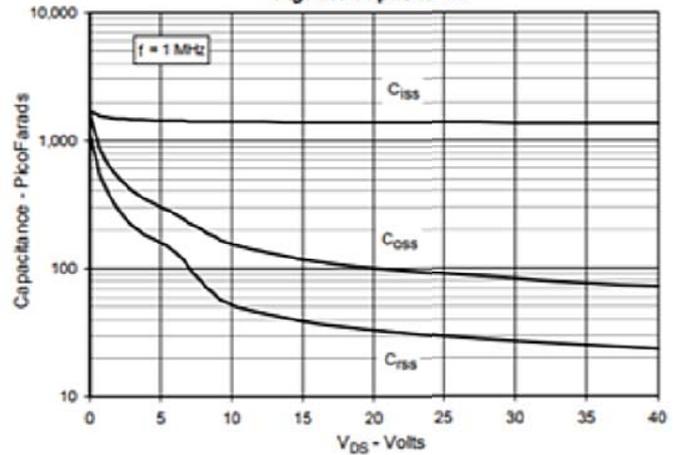


Fig. 11. Maximum Transient Thermal Impedance

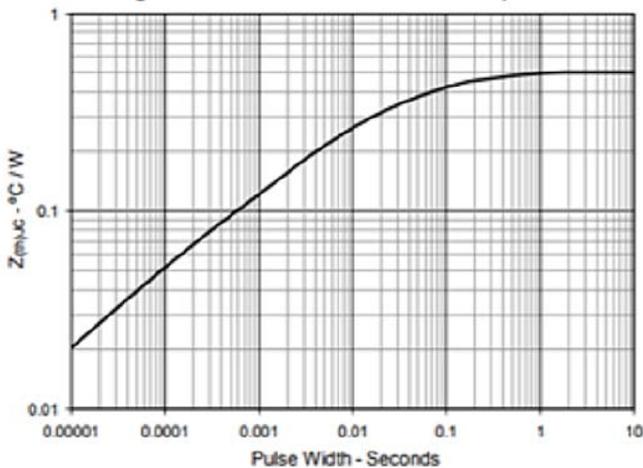
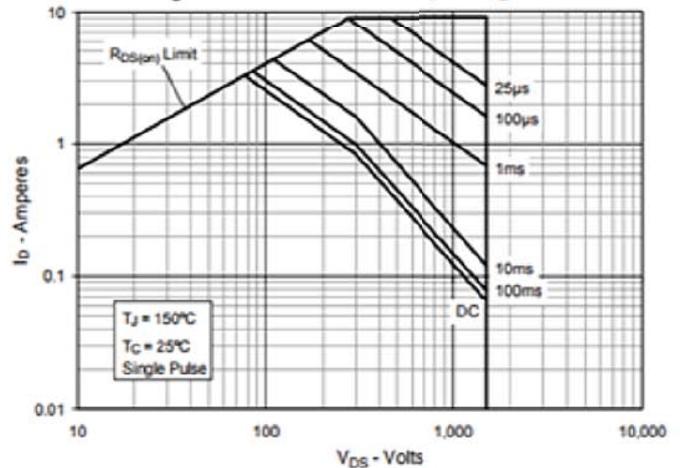


Fig. 12. Forward-Bias Safe Operating Area



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