

## DAM005N120F1

### N-Channel Enhancement Mode MOSFET

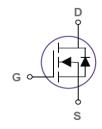
### **Features**

- · Fast Switching
- 100% Avalanche Tested
- · Improved dv/dt Capability

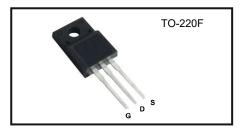
## **Applications**

- Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction(PFC)

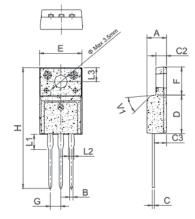
Preliminary



$V_{DSS}$	1200V
I <sub>D(@25°C)</sub>	5A
$R_{DS(ON)}$ max.	3.2Ω



Package Dimensions



	Dimensions						
Ref.	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α	4.50		4.90	0.177		0.193	
В	0.74	0.80	0.83	0.029	0.031	0.033	
С	0.47		0.65	0.019		0.026	
C2	2.45		2.75	0.096		0.108	
C3	2.60		3.00	0.102		0.118	
D	8.80		9.30	0.346		0.366	
Е	9.80		10.4	0.386		0.410	
F	6.40		6.80	0.252		0.268	
G		2.54			0.1		
Н	28.0		29.8	1.102		1.173	
L1		3.63			0.143		
L2	1.14		1.70	0.045		0.067	
L3		3.30			0.130		
V1		45°			45°		

## **Absolute Maximum Ratings**

(Tc = 25°C unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Drain Source Voltage	V <sub>DS</sub>	1200	V
Gate Source Voltage	V <sub>GS</sub>	± 30	v
Drain Current Continuous @ Tc = 25°C	lь	5	Α
Drain Current Pulsed Note2	Ірм	20	Α
Single Pulse Avalanche Energy	Eas	150	mJ
Power Dissipation @ Tc= 25°C	Po	350	w
Storage Temperature Range	Тѕтс	-55 to +150	°C
Operating Junction Temperature Range	TJ	-55to +150	°C
Thermal Resistance Junction to Case	R hetaЈс	0.6	°C/W

Note: 1. Repetitive rating pulse width limited by maximum junction temperature

2. Repetitive rating : pulse width limited by junction temperature

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## Electrical Characteristics @ Tc =25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit		
OFF Characteristics								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V · I <sub>D</sub> =0.25mA	1200	-	-	V		
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>GS</sub> =0V , V <sub>DS</sub> =1200V	-	-	5	μΑ		
Gate To Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±30V , V <sub>DS</sub> =0V	-	-	±100	nA		
ON Characteristics ( Pulse Width < 380µs, Duty Cycle < 2%.)								
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =0.25mA	3.0	4.0	5.0	V		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V , I <sub>D</sub> =1.5A	-	2.6	3.2	Ω		
Dynamic Characteristics								
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =25V	-	1206	-	pF		
Output Capacitance	C <sub>oss</sub>	V <sub>GS</sub> =0V	-	103	-			
Reverse Transfer Capacitance	C <sub>rss</sub>	Freq.=1MHz	-	22.3	-			
Switching Characteristics								
Turn-On Delay Time	t <sub>d(on)</sub>	$V_{DD}$ =600 $V$ $I_{D}$ =3 $A$ $R_{G}$ =10 $\Omega$	-	23.6	-	ns		
Rise Time	t <sub>r</sub>		-	8.6	-			
Turn-Off Delay Time	t <sub>d(off)</sub>		-	50.8	-			
Fall Time	t <sub>f</sub>		-	18	-			
Total Gate Charge	Qg	V <sub>DS</sub> =960V	-	39	-			
Gate to Source Charge	Q <sub>gs</sub>	V <sub>GS</sub> =10V	-	7.5	-	nC		
Gate to Drain Charge	$Q_{gd}$	I <sub>DS</sub> =3A	-	23.4	-			
Source-Drain Diode Characteristics								
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V • I <sub>S</sub> =3A	-	-	1.6	V		
Continuous Source Current (Body Diode)	I <sub>SD</sub>		-	-	5	Α		
Max. Pulsed Current (Body Diode)	I <sub>SM</sub>		-	-	20	Α		
Reverse Recovery Time	T <sub>rr</sub>	Is=3A	-	526	-	ns		
Reverse Recovery Charge	Qrr	dir/dt=100A/μs	-	2	-	μC		

Note : 3. Isp = 6A, di/dt  $\leq$  200A/us, Vpp  $\leq$  BVps, Start TJ =25 $^{\circ}$ C



### **Typical Performance Characteristics**

Figure 1. Output Characteristics

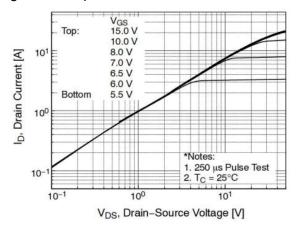


Figure 3. Drain-to-Source On Resistance vs Drain Current

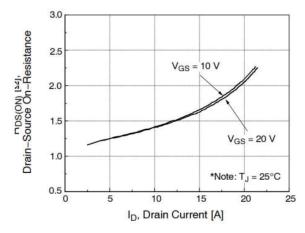


Figure 5. Gate Charge Characteristics

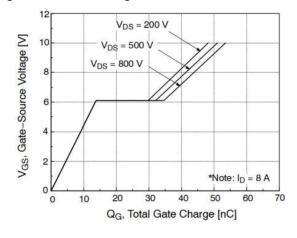


Figure 2. Transfer Characteristics

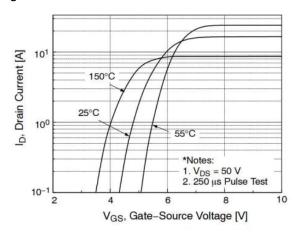


Figure 4. Body Diode Forward Voltage vs Source Current and Temperature

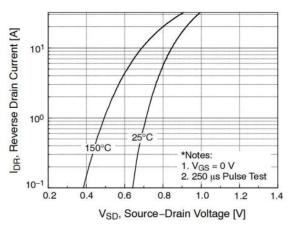
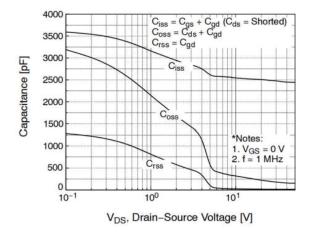


Figure 6. Capacitance Characteristic



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### **Typical Performance Characteristics**

Figure 7. Normalized Breakdown Voltage vs Junction Temperature

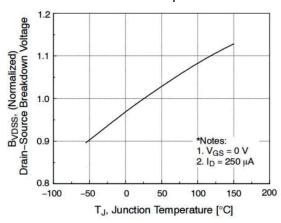


Figure 9. Maximum Continuous Drain Current vs Case Temperature Temperature

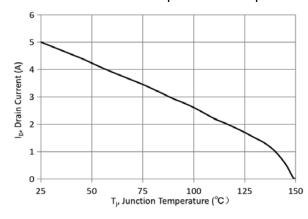


Figure 8. Normalized On Resistance vs Junction Temperature

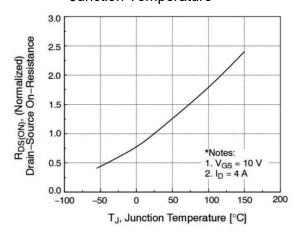
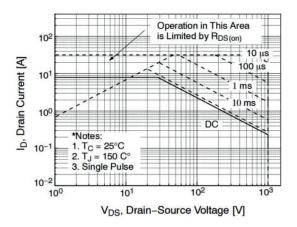
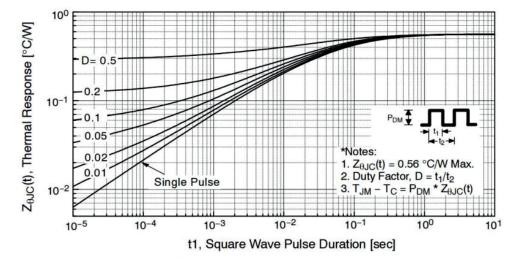


Figure 10. Maximum Safe Operating Area



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Figure 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case







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